PTO/SB/21 (08-00)
Approved for use through 10/31/2002. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

09/539,517

2181

March 30, 2000

Albert R. Nelson

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Application No.

First Named Inventor **Group Art Unit** 

Filing Date

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

	Examiner Name	<b>R</b> . 1	Dharia Charia							
Total Number of Pages in This Submissi	on 14	Attorney Docket Numb	er 423	Dharia JAN 3 CE						
ENCLOSURES (check all that apply)										
Fee Transmittal Form	Assignmen (for an App	t Papers lication)		After Allowance Communication to Group						
Fee Attached	Drawing(s)			Appeal Communication to Board of Appeals and Interferences						
Amendment / Response	Licensing-r	elated Papers		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief,						
After Final Affidavits/declaration(s)	Petition			Proprietary Information						
Extension of Time Request	Petition to 0 Provisional			Status Letter						
Express Abandonment Request	Power of A	ttorney, Revocation Correspondence Addres	s X	Other Enclosure(s) (please identify below):						
Information Disclosure Statement	Terminal D	sclaimer		- check in the amount of \$146.00						
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Response to Missing Parts/	CD, Numbe	r of CD(s)								
☐ Incomplete Application ☐ Basic Filing Fee	Remarks									
Declaration/POA		_								
Response to Missing Parts under 37 CFR 1.52 or 1.53										
SIGNATURE	OF APPLICAN	T, ATTORNEY, OR	GENT	Γ						
Firm Gregory D. Caldwell, Reg. No. 39,926										
Individual name BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP										
Signature										
Date January 21, 2003										
CERTIFICATE OF MAILING/TRANSMISSION										
I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail with sufficient postage in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on:  January 21, 2003										
Typed or printed name Annie G. Pearson										
Signature Ami i	Poour	<u> </u>	Date	January 21, 2003						

PTO/SB/17 (01-03)
Approved for use through 04/30/2003. OMB 0651-0032
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Application Number

Complete if Known

09/539,517

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Effective 01/01/2003. Patent fees are subject to annual revision.							I			1	haria	)II		
Applicant claims small entity status. See 37 CFR 1.27.													- 1V'	
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**or number previously paid, if greater, For Reissues, see below									J. 30. C		•	202101AE (9)	(\$)	110.00
SUBMITTED BY Complete (if applicable)														
Name (Print/Type) Gregory D. Caldwell							legistratio httorney/Age		3	9,926	Telephone	(503) 6	84-6200	
Signature Gregory D. Caldwell										D-4-	01/	21/03		
Signature											Date	I 31/		

TRANSMIT



4/A C. Amai 1/28/03

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Albert Rudy NELSON

Serial No.:

09/539,517

Group Art Unit:

2181

Filed:

March 30, 2000

Examiner:

R. Dharia

The Children of the Content of the Children of the

FOR:

LOW PIN COUNT (LPC) FIRMWARE HUB RECOVERY

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

Assistant Commissioner for Patents Washington, DC 20231

Sir:

In response to the Office Action mailed September 23, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please replace the paragraph on page 1, lines 20-25 with the following paragraph:

The large number of pins needed to support the ISA bus and X-bus standards

generally increase the overall system cost. For example, larger packages are required for a CPU or chip-set. The development of the low pin count (LPC) bus has obviated to some extent the problem mentioned above. The LPC bus includes general purpose signal lines that carry substantially all time-multiplexed address, data and control information to

implement memory, I/O, and bus transactions between the CPU and other system

devices.

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